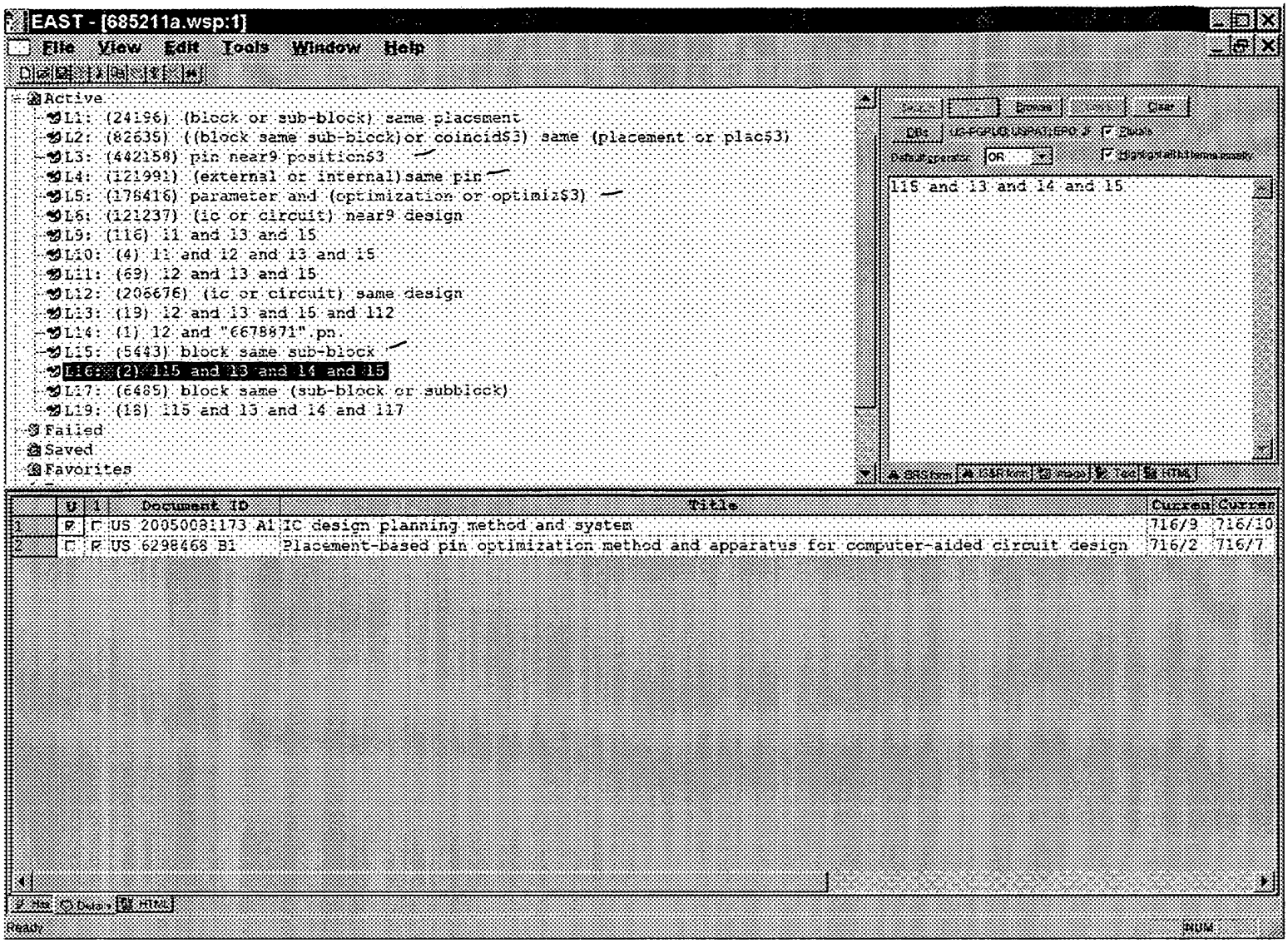
EAST - [685211a.wsp:1]
File View Edit Tools Window Help
Active
L1: (24196) (block or sub-block) same placement
L2: (82635) ((block same sub-block)or coincid$3) same (placement or plac$3)
L3: (442158) pin near9 position$3
L4: (121991) (external or internal)same pin
L5: (176416) parameter and (optimization or optimiz$3)
L6: (121237) (ic or circuit) near9 design
L9: (116) l1 and l3 and l5
L10: (4) l1 and l2 and l3 and l5
L11: (69) l2 and l3 and l5
L12: (206676) (ic or circuit) same design
L13: (19) l2 and l3 and l5 and l12
L14: (1) l2 and "6678871".pn.
L15: (5443) block same sub-block
L16: (2) l15 and l3 and l4 and l5
L17: (6485) block same (sub-block or subblock)
L19: (18) l15 and l3 and l4 and l17
Failed
Saved
Favorites
Browse
Clear
DBs
Default operator:
OR
l15 and l3 and l4 and l5
Document ID
Title
Curren
Curren
1 US 20050081173 A1 IC design planning method and system 716/9 716/10
2 US 6298468 B1 Placement-based pin optimization method and apparatus for computer-aided circuit design 716/2 716/7
Ready
NUM